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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2823
Examiner: Suk San Foong

INFORMATION
DISCLOSURE STATEMENT

LECHNOLOGY, CEV

5-91-03

In Re PATENT APPLICATION Of:

Applicant(s) : Yasuo TANAKA

Serial No. : 09/660,484

Filed: September 12, 2000

For: METHOD OF MANUFACTURING

SEMICONDUCTOR DEVICE

Attorney Ref.: OKI 262

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(c), i.e., after a first Office Action on the merits but before Final Rejection and/or Notice of Allowance. Pursuant to 37 C.F.R. § 1.97(e)(2), it is certified that each item submitted with this Information Disclosure Statement was cited in an Office Action from the Japanese Patent Office in a counterpart foreign application on March 18, 2003, that is, less than three months prior to filing this Information Disclosure Statement.

Attached are copies of a Japanese Office Action and seven Japanese Patents with English translations, and one U.S. document therein. The U.S. patent is a translation of Japanese Application No. 10-79362. The relevance of the publications can be gleaned from the attached English language Abstracts. The documents are listed on the attached Form PTO-1449.

A check in payment of the \$180.00 fee under 37 C.F.R. §1.17(p) is also enclosed. If the check is missing, or made out for an insufficient amount, please charge our deposit account, No. 18-0002, for any deficiency and notify us accordingly.

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Consideration of the submitted documents is respectfully requested.

Respectfully submitted,

Phillip G. Avruch - Reg. No. 46,076

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PGA:tlc

May 6, 2003

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